

CS315-01 RISC-V Emulation 1

Lab 03 due Tue Oct 1 11:59 pm

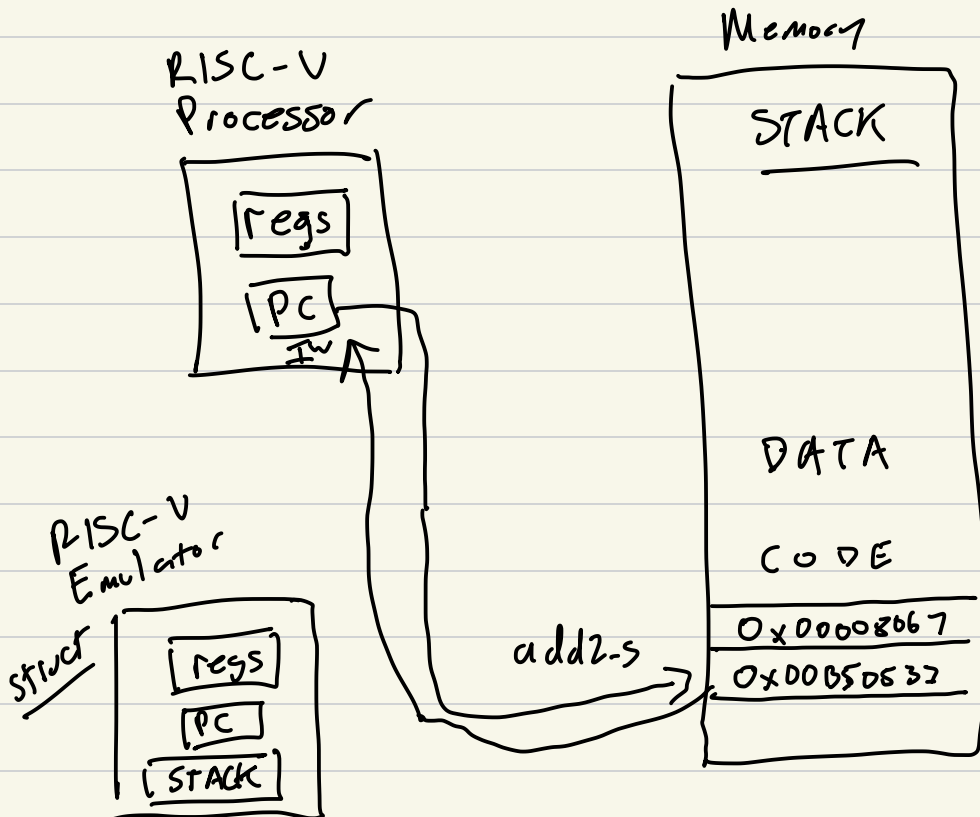
Lab 03 Exam Problems

due Thu Oct 3 11:59 pm

Project 04 due Tue Oct 8 11:59 pm

Project 04 I&R Wed Oct 9

Midterm Thu Oct 10



Processor State

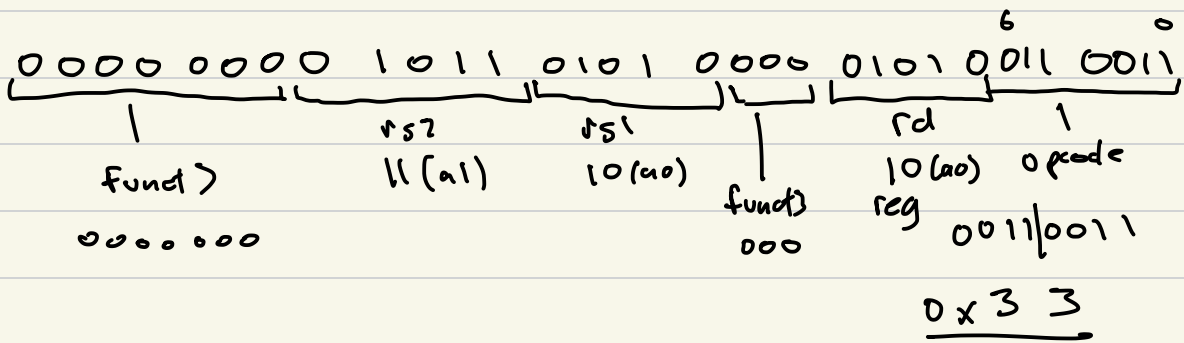
Registers (32)

PC Program Counter

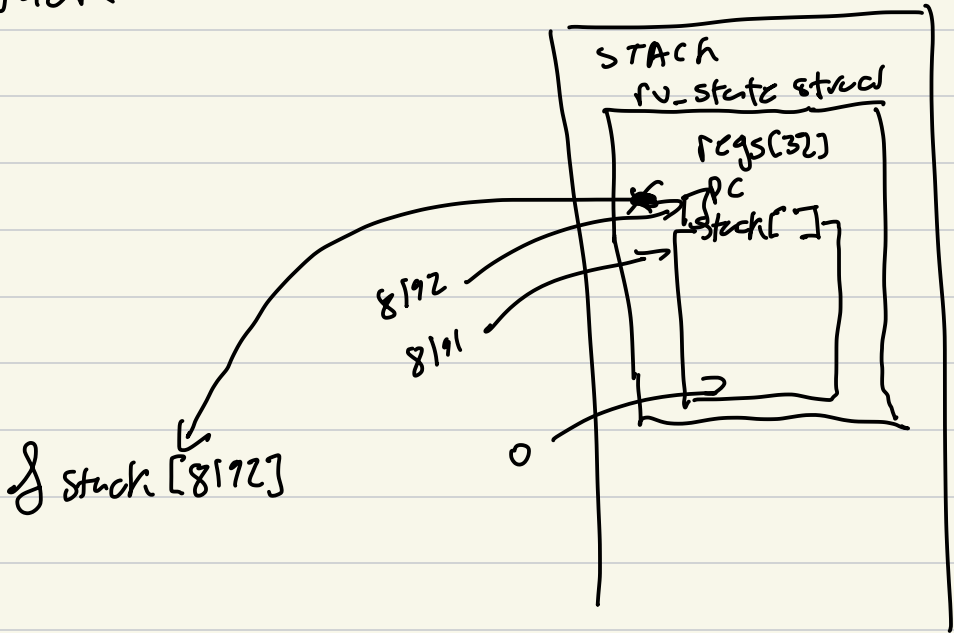
Memory - STACK

add rd a0, ^{rs1} a0, ^{rs2} a1

iw = 0x 06 65 05 33



Stack



get_bits (v , start , count)

$$rd = (iw \gg 7) \& \underline{0b1111}$$

$$r = (v \gg start) \&$$

$$get_bits (iw , 7 , 5)$$

Branch

